

Received: 8/29/01 10:00

317 638 2138 -> LSI LOGIC Page 3

08/29/01 WED 12:06 FAX 317 638 2139

MAGINOT ADDISON & ASSOCIATES

003

Page 1 f2

Express Mailing Label No. EL904476829US

Attorney Docket No. 01-0146

PATENT

DECLARATION, POWER OF ATTORNEY, AND PETITION

As a below named inventor, I hereby declare that:

My residence, post office address, and citizenship are as stated below next to my name.

I believe I am the original, first, and sole inventor (if only one name is listed below) or an original, first, and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled "Arrangement and Method for Fabricating a Semiconductor Wafer"

☒ is attached hereto.

was filed on _____ as Application Serial No. _____, and was amended on _____ (if applicable)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, § 1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

PRIOR FOREIGN APPLICATION(S)			Priority Claimed
(Number)	(Country)	(Date Filed)	Yes ___ No ___

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56(a), regarding events which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.)	(Filing Date)	(Status)

(Application Serial No.)	(Filing Date)	(Status)

I hereby declare that all statements made of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

I hereby appoint Timothy R. Croll, Reg. 36,771; Leo J. Peters, Reg. 33,562; Peter P. Scott, Reg. 33,279; Sandeep Jaggi, Reg. 43,331; Bradford G. Addison, Reg. 41,486; as my attorneys with full power of substitution and revocation, to prosecute this application and to transact all business in the U.S. Patent and Trademark Office connected therewith and before competent international authorities.

317 638 2138 -> LBI L...; Page 4

MAGINOT ADDISON & MORE

004

Post Office Address 30MP

THE